

RapidIO: The Embedded System Interconnect By Sam Fuller

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MECS Time Synchronization protocol for smaller embedded systems. The RapidIO interconnect is used extensively in the following applications: Wireless base stations;

Rapidio - xilinx

An Embedded System Component it has no impact on operating system software. The RapidIO interconnect is designed for common .25 and .18 micron CMOS

Sub-microsecond interconnects: pcie, rapidio and

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design, embedded system design Revision 1.2 of the RapidIO Interconnect Specification and is targeted Sam Fuller, president of the RapidIO Trade

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Rapidio: optimized for low-latency processor

Sam Fuller RapidIO Trade Association RapidIO was designed to serve as a low-latency processor interconnect for use in embedded systems Sam Fuller is a founder

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Ibm joins rapidio trade association to promote

the speed at which components inside embedded systems The RapidIO Trade Association is an changing needs," said Sam Fuller, RapidIO

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I've been reading the spec and also "RapidIO The Embedded System Interconnect" by Sam Fuller and had seen this since writing my original post.

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Sam Fuller is the author of RapidIO (0.0 avg rating, 0 ratings, 0 reviews, published 2004), Rapidio (0.0 avg rating, 0 ratings, 0 reviews,

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